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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application / Conf. No.	10/769,591 / 8956	
			Filing Date	January 30, 2004	
			First Named Inventor	Chidamber R. Kulkarni	
			Art Unit	Binh C. Tat	
			Examiner Name	2825	
Sheet	2	of	3	Attorney Docket Number	X-1557-3 US

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. *	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
BK		US- 2005/0172085 A1	08-04-05	Klingman	
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FOREIGN PATENT DOCUMENTS						
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Examiner Signature		Date Considered	11/28/06
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		Attorney Docket Number	X-1557-3 US
Sheet	3	of	3

OTHER - NON PATENT LITERATURE DOCUMENTS			
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BT		Gordon Brebner; "Multithreading for Logic-Centric Systems"; 12th International Conference on Field Programmable Logic and Applications; Montpellier, France; 2-4 September 2002; Springer LNCS 2438; pp. 5-14.	
		Gordon Brebner; "Single-chip Gigabit Mixed-version IP Router on Virtex-II Pro"; Proceedings of the 10th Annual IEEE Symposium on Field-Programmable Custom Computing Machines (FCCM'02); April 2002; IEEE Computer Science Press; pp. 35-44.	
		Xilinx, Inc.; U.S. Patent Application Serial No. 10/402,659 filed on March 28, 2003 by James-Roxyby.	
		Xilinx, Inc.; U.S. Patent Application Serial No. 10/420,652 filed on April 21, 2003 by Brebner.	
BT		Xilinx, Inc.; U.S. Patent Application Serial No. 10/421,013 filed on April 21, 2003 by Brebner.	

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